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Docket No. 740819-634

#7/A  
8-13-02  
Mullish

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of: )  
Koji ARITA et al. ) Art Unit: 2813  
Serial No. 09/938,528 )  
Filed: 08/27/2001 ) Examiner: D. Blum  
For: METHOD FOR MANUFACTURING A )  
SEMICONDUCTOR DEVICE )

CERTIFICATE OF MAILING

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9.30.2002

*Adele M. Stamper*  
Adele M. Stamper

AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action mailed April 1, 2002, please amend the above identified application as follows.

IN THE CLAIMS:

✓  
Please cancel claims 2 and 3.

Please amend claim 1 as follows:

SUB  
B1  
A1  
1. (Amended) A method for manufacturing a semiconductor device, comprising the steps of:

forming, in a semiconductor layer formed on a first insulating film, an element isolation groove extending to the first insulating film;